



Attorney Docket No. GE 2302000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

U.S. application of: Alan J. Janicek, et al.

For: **QUICK-CONNECT POSITIVE TEMPERATURE
COEFFICIENT OF RESISTANCE RESISTOR/OVERLOAD
ASSEMBLY AND METHOD**

U.S. Serial No.: 09/682,765

Filed: October 16, 2002

Group Art Unite: 2832

Examiner: Karl Easthom

Group Art Unit:

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Assistant Commissioner for
Patents
Washington, D.C. 20231

Dear Sir:

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231, on <u>10-22-02</u>	
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Signature	

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

This is a response to the restriction/election requirement mailed **October 1, 2002**, in the above-captioned patent application. The Examiner identified three (3) groups of claims, Group I, Claims 1-10, drawn to a resistor, classified in class 338, subclass 22R; Group II, Claims 11-20, drawn to a method of making a resistor, classified in class 29, subclass 620; and Group III, Claim 21, drawn to a method of using a resistor, classified in class 29, subclass 620.

Applicants respectfully traverse this restriction, however **Applicants elect the Claims of Group I, claims 1-10**, drawn to a resistor to prosecute in the present application as required by the Examiner.

Respectfully submitted,

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